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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant Jaroslav Hyncek

Docket Number: TI-36483

Serial No.: 10/633,993

Art Unit: 2826

Filed: 08/04/03

Examiner: Johannes P. Mondt

For: Clocked Barrier Virtual Phase Charge Coupled Device
Image Sensor

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below:

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<input type="checkbox"/> CONTINUATION APPN	<input checked="" type="checkbox"/> ELECTION
<input type="checkbox"/> DIVISIONAL APPN	
NAME OF INVENTOR(S): Jaroslav Hyncek	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/633,993 Filing Date: 08/04/03	
TITLE OF INVENTION: Clocked Barrier Virtual Phase Charge Coupled Device Image Sensor	
TI FILE NO.: TI-36483	DEPOSIT ACCT. NO.: 20-0668
FAXED: 1-3-05 DUE: 1-17-05 ATTY/SECY: AKS/kjv	

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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265-6474

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Applicant: Jaroslav Hynecsek

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For: Clocked Barrier Virtual Phase Charge Coupled Device Image Sensor

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

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Karen Vertz
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Date

ELECTION

This election is offered in response to the Examiner's restriction requirement mailed December 17, 2004.

Applicant hereby elects to pursue Species 2: Figure 3, pages 10-11, claims 11-17, without traversing the Examiner's restriction requirement.

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Respectfully submitted,



Alan K. Stewart
Attorney for Applicant
Registration No. 35,373